L Number	Hits	Search Text	DB	Time stamp
1	41231	first adj3 mode	USPAT	2004/03/16 00:44
2	39117	second adj3 mode	USPAT	2004/03/16 00:44
3	30604	(first adj3 mode) and (second adj3 mode)	USPAT	2004/03/16 00:44
4	14738	read\$3 adj2 mode	USPAT	2004/03/16 00:45
5	10898	writ\$3 adj2 mode	USPAT	2004/03/16 00:45
6	1634	\$refresh\$3 adj2 mode	USPAT	2004/03/16 00:46
7	18863	(read\$3 adj2 mode) or (writ\$3 adj2 mode)	USPAT	2004/03/16 00:46
8	608	((read\$3 adj2 mode) or (writ\$3 adj2 mode))	USPAT	2004/03/16 00:46
		and (\$refresh\$3 adj2 mode)		
9	31092	((first adj3 mode) and (second adj3 mode))	USPAT	2004/03/16 00:48
		or (((read\$3 adj2 mode) or (writ\$3 adj2		
1		mode)) and (\$refresh\$3 adj2 mode))		
10	214823	control\$4 adj2 circuit	USPAT	2004/03/16 00:49
11	8830	(((first adj3 mode) and (second adj3 mode))	USPAT	2004/03/16 00:49
]		or (((read\$3 adj2 mode) or (writ\$3 adj2		
		mode)) and (\$refresh\$3 adj2 mode))) and		
		(control\$4 adj2 circuit)		
12		mode adj2 trigger adj2 signal	USPAT	2004/03/16 00:50
13	3	first adj2 (mode adj2 trigger adj2 signal)	USPAT	2004/03/16 00:50
14	6	second adj2 (mode adj2 trigger adj2 signal)	USPAT	2004/03/16 00:51
16	0	(first adj2 (mode adj2 trigger adj2 signal))	US-PGPUB;	2004/03/16 00:51
		and (second adj2 (mode adj2 trigger adj2	EPO; JPO;	:
		signal))	DERWENT;	
, , ,		/6:3:0 /3:0:3:0 -:	IBM_TDB	2004/02/16 00 52
15	2	(first adj2 (mode adj2 trigger adj2 signal))	USPAT	2004/03/16 00:53
		and (second adj2 (mode adj2 trigger adj2		
1.7	0	signal))	USPAT	2004/03/16 00:53
17	U	<pre>((((first adj3 mode) and (second adj3 mode)) or (((read\$3 adj2 mode) or (writ\$3 adj2</pre>	USPAI	2004/03/16 00:53
!		mode)) and (\$refresh\$3 adj2 mode))) and		
		(control\$4 adj2 circuit)) and ((first adj2		
		(mode adj2 trigger adj2 signal)) and (second		
		adj2 (mode adj2 trigger adj2 signal)))		
18	n .	((((first adj3 mode) and (second adj3 mode))	US-PGPUB;	2004/03/16 00:53
13		or (((read\$3 adj2 mode) or (writ\$3 adj2	EPO; JPO;	2001, 03, 10 00.33
		mode)) and (\$refresh\$3 adj2 mode))) and	DERWENT;	
		(control\$4 adj2 circuit)) and ((first adj2	IBM TDB	
		(mode adj2 trigger adj2 signal)) and (second		
j		adj2 (mode adj2 trigger adj2 signal)))	İ	
		[]	4	L